

2/10

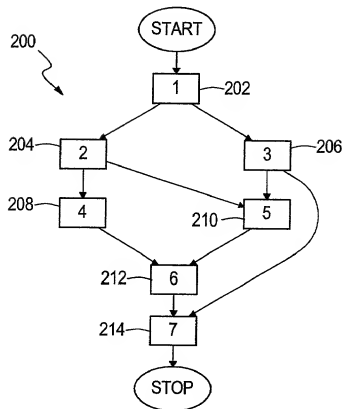


FIG. 2

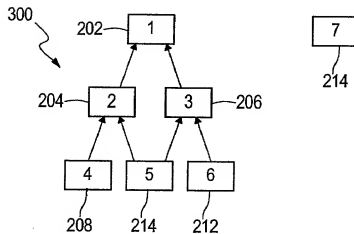


FIG. 3

3/10

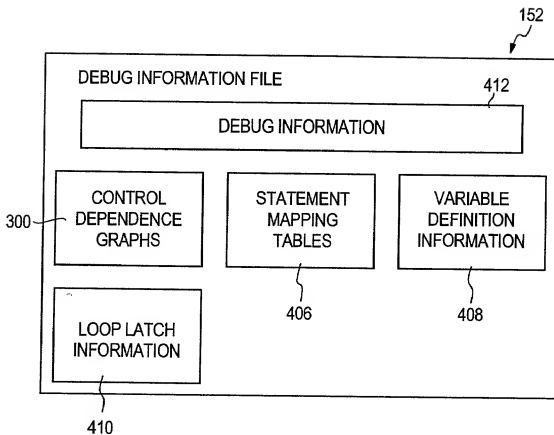


FIG. 4

SOURCE LINE NUMBER	INSTRUCTION ADDRESS	BASIC BLOCK IDENTIFIER
1	0x10ac40	1
2	0x10ac54	1
4	0x10ac58	2
5	0x10ac70	2
8	0x10ac78	3
9	0x10ac84	3

FIG. 6

4/10

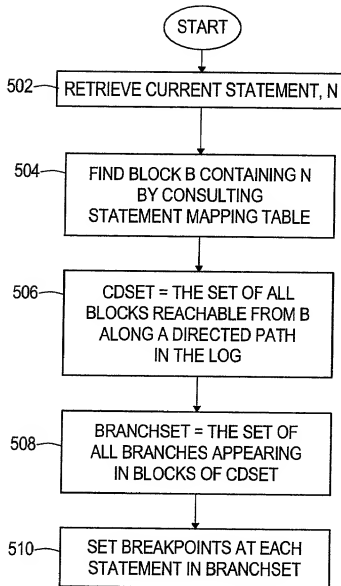


FIG. 5

0927407.002101

5/10

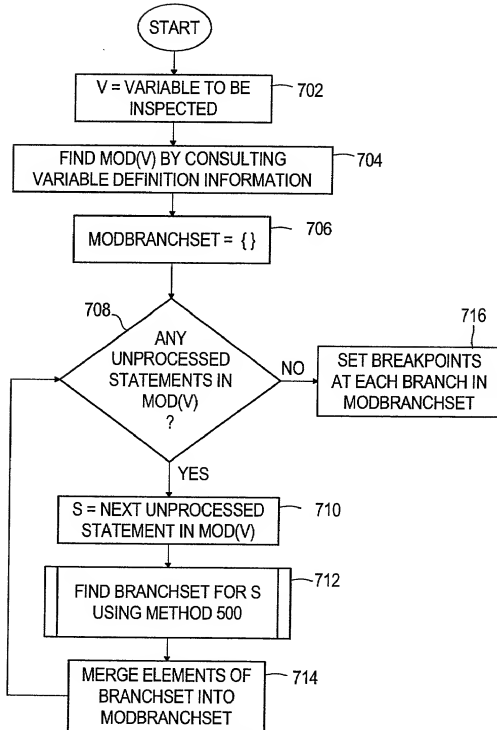


FIG. 7

6/10

802 VARIABLE V	804 STATEMENTS MODIFYING VARIABLE V
I	{5,12,17,23}
J	{10}
K	{ }
M	{12,13,90}

FIG. 8

902 BASIC BLOCK	904 LOOP HEADERS
1	-
2	-
3	{3}
4	{3}
5	{5,3}
6	{5,3}
7	{3}

FIG. 9

904 LOOP HEADER	1004 LOOP LATCHES
3	{4,7}
5	{5,6}

FIG. 10

7/10

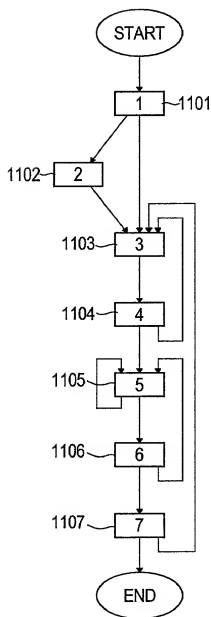


FIG. 11

8/10

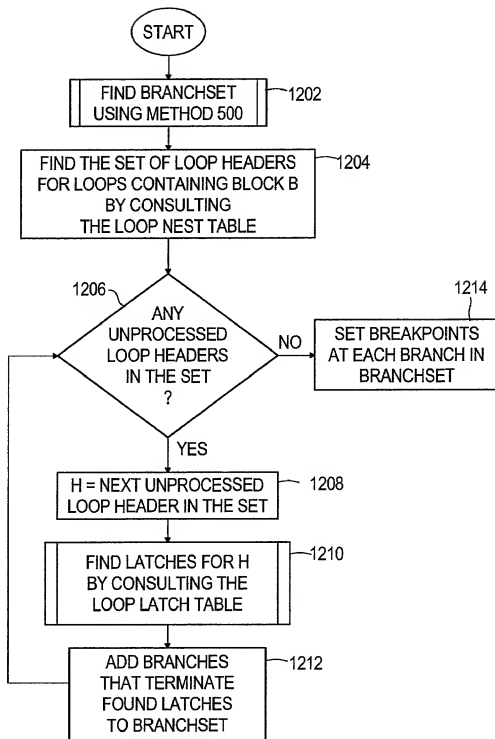


FIG. 12

9/10

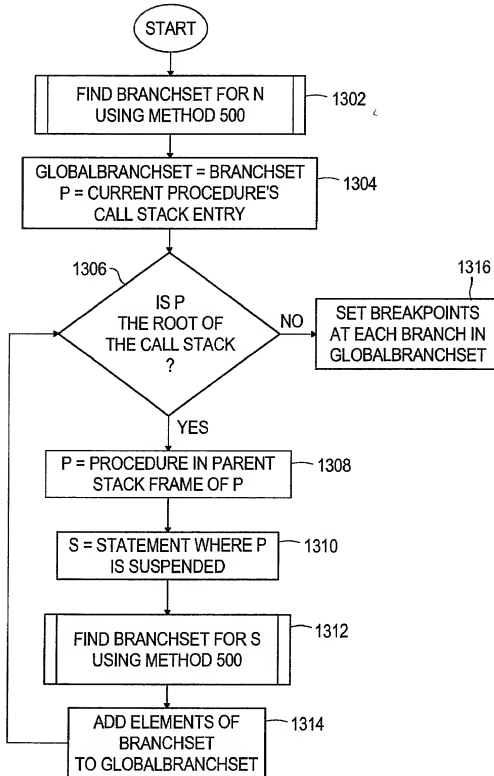


FIG. 13

10/10

1402	1404
procC	top of stack
procA	22
process	15
main	47

FIG. 14